# WILFRED KISKU

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I intend to work in a challenging environment having a thirst of knowledge in the field of VLSI Systems and Embedded Systems Design and maintain excellent interpersonal communication, time management, and problem resolution skills.

## EDUCATION

**Sardar Vallabhai National Institute of Technology Surat** *July 2017 - May 2019*
Post Graduate CGPA: 7.6
Department of Electronics {VLSI and Embedded System}

**Mount Zion College of Engineering and Technology Pudukkottai** *August 2003 - April 2007*
Under Graduate Overall Percentage: 75.76
Department of Electronics {Electronics and Communication}

**Raymond Memorial Higher Secondary School Falakata** *April 2002 - March 2003*
Indian School Certificate Examination Overall Percentage: 65.50

**S.D.A Senior Secondary School Khunti** *April 2000 - March 2001*
Indian Certificate for Secondary Examination Overall Percentage: 75.50

## TECHNICAL STRENGTHS

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|---|---|
| **Computer Languages** | C, C++, Embedded C, Assembly Language Programming (x86 Microprocessor and 8051 Micro-controller), JAVA, Python, Verilog, VHDL, HTML5, CSS3, Javascript, PHP, MySQL, Bootstrap. |
| **OS Platform** | Linux, Windows 8/7/XP/2000, Raspian (Raspberry Pi). |
| **Software & Tools** | Xilinx ISE, Xilinx Vivado, Cadence, Mentor Graphics, NgSpice, PSpice, Sentaurus TCAD , Logisim, Keil Microvision3, Matlab 6, Octave. |
| **Hardware Kits** | Zedboard Zynq-7000 SoC, Zybo Zynq-7000 SoC, EDGE Artix 7 FPGA, Arduino UNO, Raspberry Pi Model B+, Easy 8051 v6. |

## EXPERIENCE

**Higher Secondary School** July 2009 - April 2016
*Freelance Teaching*

- Employed as a Teacher in Higher Secondary School affiliated to ICSE and ISC Board, New Delhi.
- Subjects Taught: Computer Application (Classes IX - X), Computer Science (classes XI - XII), Mathematics (Classes XI - XII).

## ACHIEVEMENTS

**EVOLUTION II**, State Level Symposium (MZCET) First Place in the Debate Competition.

**DE NOVO**, Intra College Symposium (MZCET) First Place in E-Advertisement Competition.

**Zonal Level Badminton Tournament** (Anna University) Third Position in the Anna University Zonal Badminton Tournament.

## PROJECTS

**VLSI Architectures for Deep Learning** August 2018 - June 2019
***Title:*** *Implementation of Generic Object Tracking on an FPGA using the YOLO/GOTURN Tracker MTech Dissertation*

· Description: Implementation of Deep Learning Architectures on an FPGA for a Generic Object Tracking and optimization of these architectures using HLS in Xilinx Vivado.

**Baugh Wooley Multipler** August 2017 - March 2018
***Title:*** *Layout and Design of Baugh Wooley Multiplier using SCL 180nm technology* *PG Project*

· Description: The design is for a minimum area and minimum delay 4bit x 4bitBaugh Wooley array multiplier using CMOS logic and to perform pre and post layout simulation using Cadence.

**Fabrication process of a PN JUNCTION (IN4007)** August 2017 - March 2018
***Title:*** *Design of IN4007 PN Junction Diode* *PG Project*

· Description: Design and fabrication of a PN Junction diode using Sentaurus Process and Sentaurus Device to simulate and obtain the device characteristics.

**Serial Peripheral Interface Design through HDL** August 2017 - March 2018
***Title:*** *SPI Design using HDL* *PG Project*

· Description: Implemented using VHDL coding, efficiently and synthesized using tool. Simulated and analyzed by creating test bench using Xilinx ISE.

**Smart Antennas** January 2007 - March 2007
***Title:*** *Estimation of DoA using Smart Antennas (Adaptive Arrays)* *UG Project*

· Description: The project comprised of simulating Adaptive Array Antenna(using Loop antenna arrays) and design of a system of Directional Antennas that would be used to detect the DOA using Matlab.

**Azimuth and Elevation Control with Arduino**
***Title:*** *Stepper Motor based tracker with azimuth and elevation control* *Mini Project*

· Description: A system designed to track the solar energy radiated through the sun with the actuating signal as the light energy, the motors are used to control the azimuth and the elevation to direct the solar panel towards the direction so as to maximize energy harness.

## INTERNSHIPS / TRAININGS

Attended the workshop on **"Verification of VLSI Systems"** conducted by the **IEEE Students Chapter** of Sardar Vallabhai National Institute of Technology, Surat.

## FIELD OF INTEREST

Machine Learning, Robotics, UAVs, Web Development, FPGAs, Raspberry Pi and Arduino kit related projects.

## DECLARATION

I, hereby declare that the above written particulars are true to my knowledge.

Place: SVNIT, Surat Wilfred Kisku
Date: 5 June, 2019